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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: Takuya FUTATSUYAMA, et al.

SERIAL NUMBER: 10/692,799

GROUP: 2824

FILED: October 27, 2003

EXAMINER: TRAN, ANDREW Q

FOR: NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE WITH REDUCED  
CHIP REAL ESTATE AREA FOR TRANSFER TRANSISTORS

**REQUEST TO CORRECT TITLE OF INVENTION**

MAIL STOP ISSUE FEE  
COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
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SIR:

In the matter of the above-identified application for patent, we hereby request  
correction of your records to reflect the correct title of the invention. The title of the  
invention should read as follows: NON-VOLATILE SEMICONDUCTOR MEMORY  
DEVICE WITH REDUCED CHIP REAL ESTATE AREA FOR TRANSFER  
TRANSISTORS.

Respectfully Submitted,

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